

REPLACEMENT SHEET

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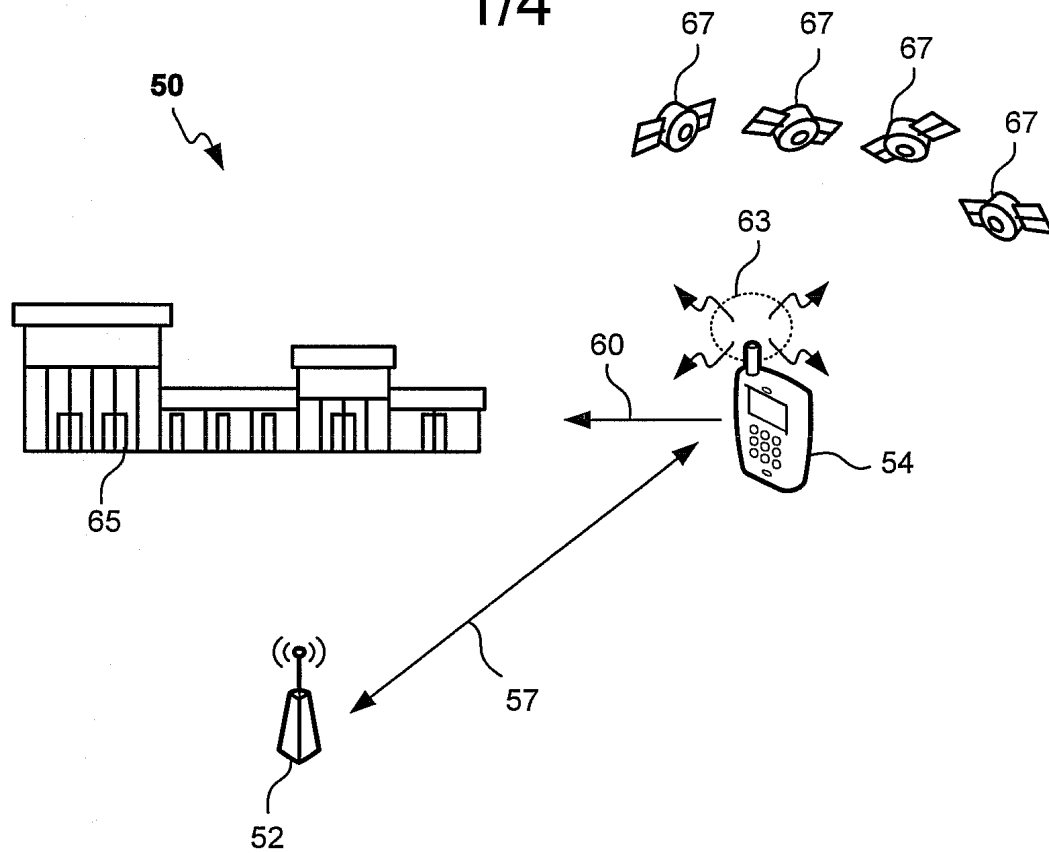
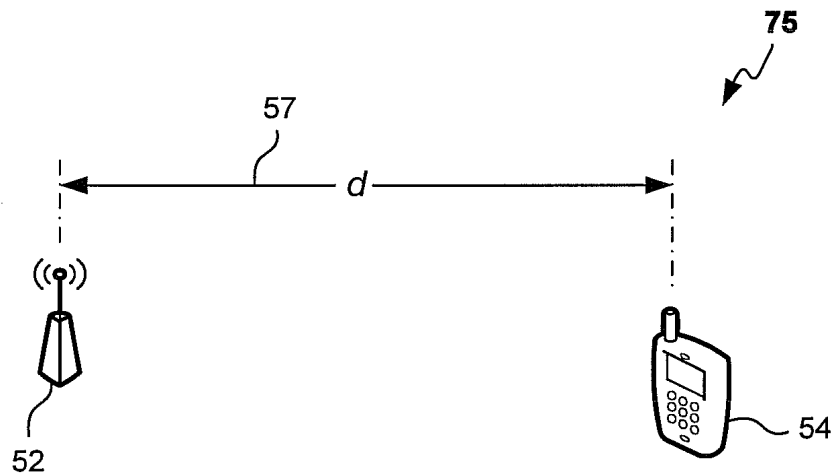


Fig. 1



$$\text{Estimated Chip Delay} \approx \frac{d}{\text{Speed of Light}}$$

Fig. 2

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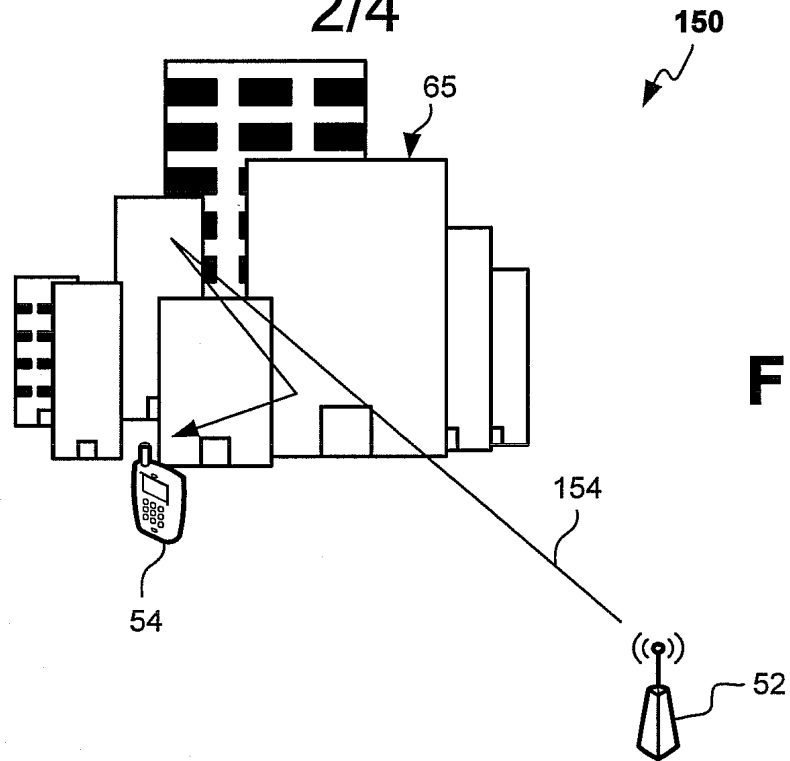


Fig. 3

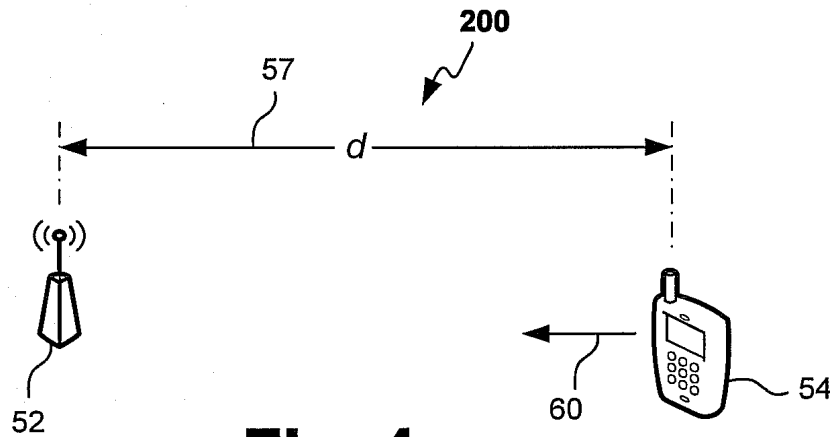


Fig. 4

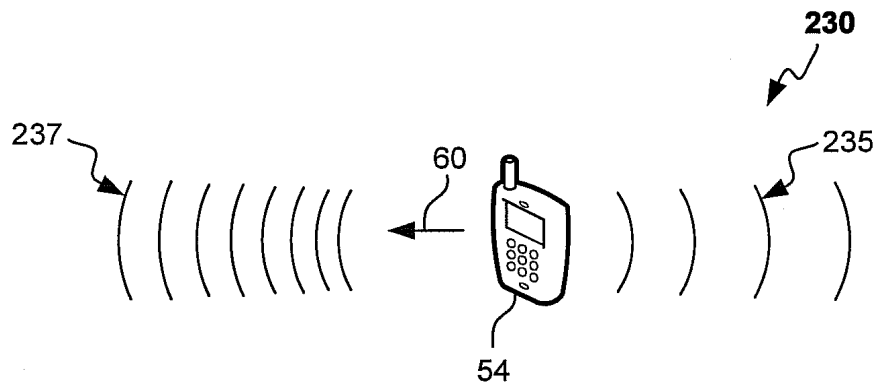
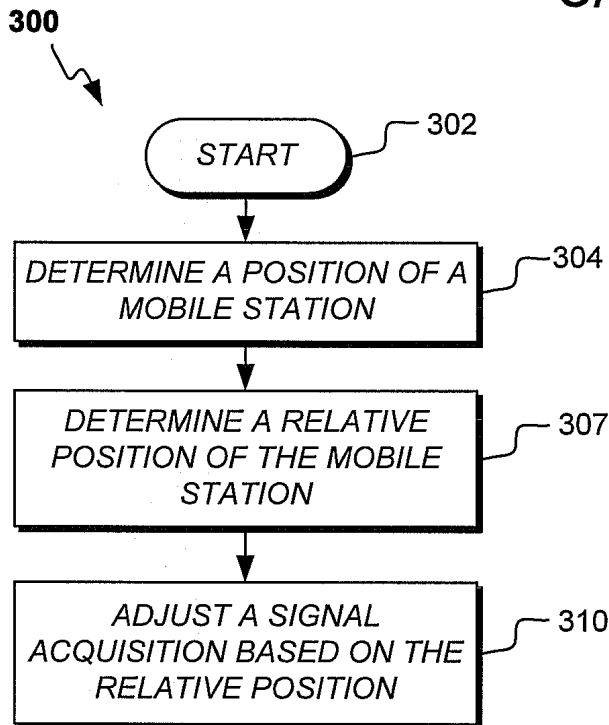
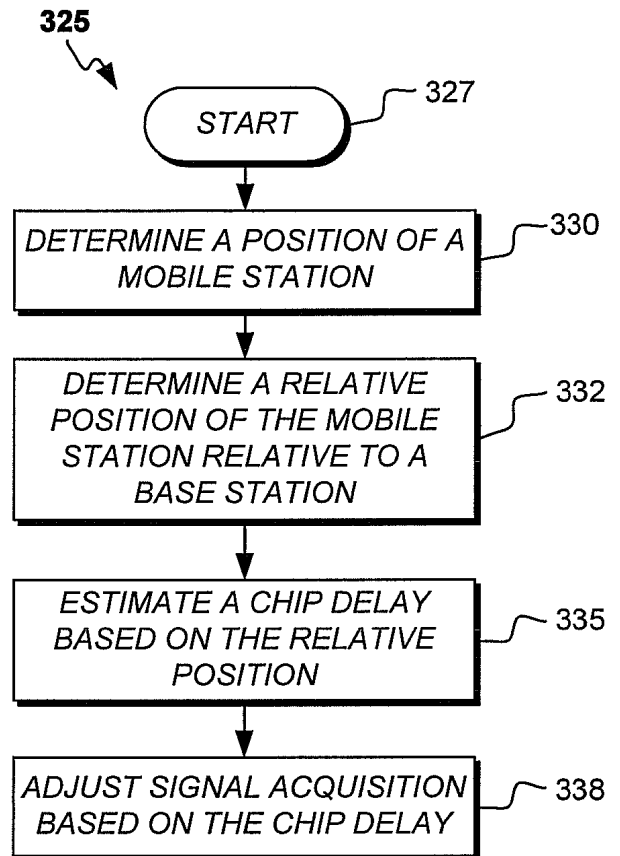
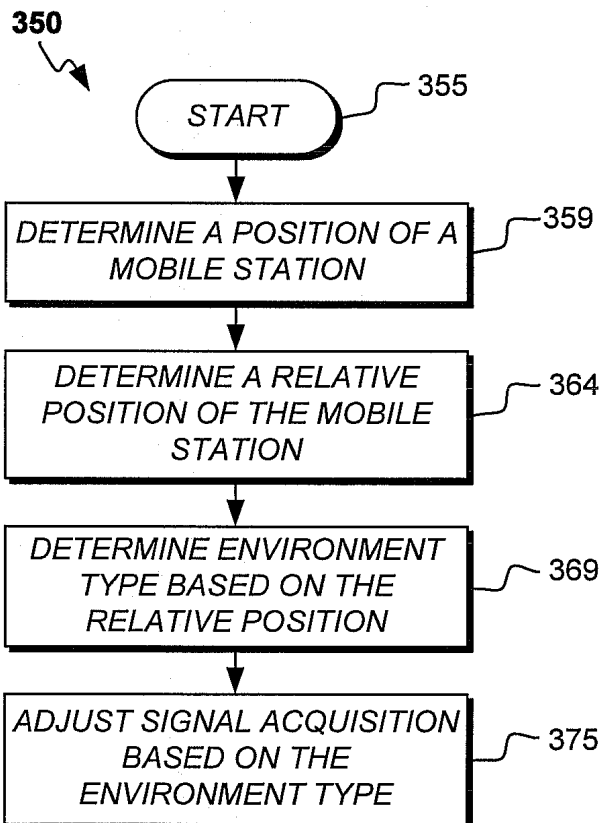
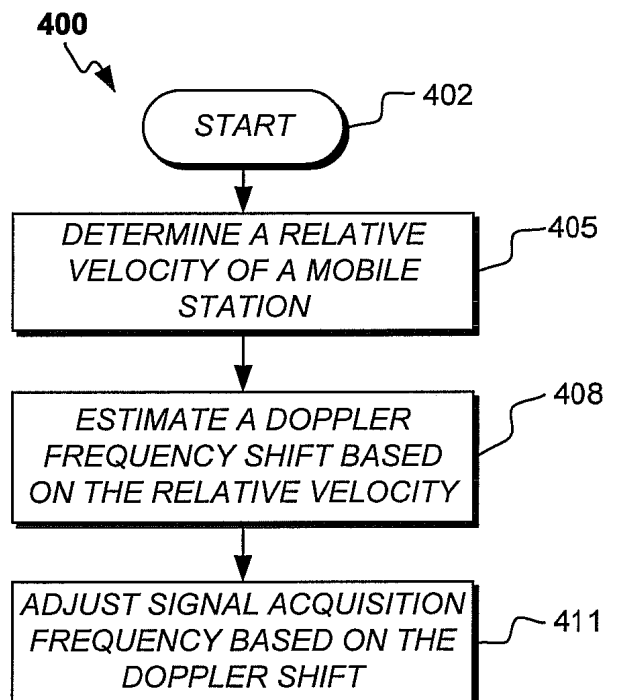


Fig. 5

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**Fig. 6****Fig. 7****Fig. 8****Fig. 9**

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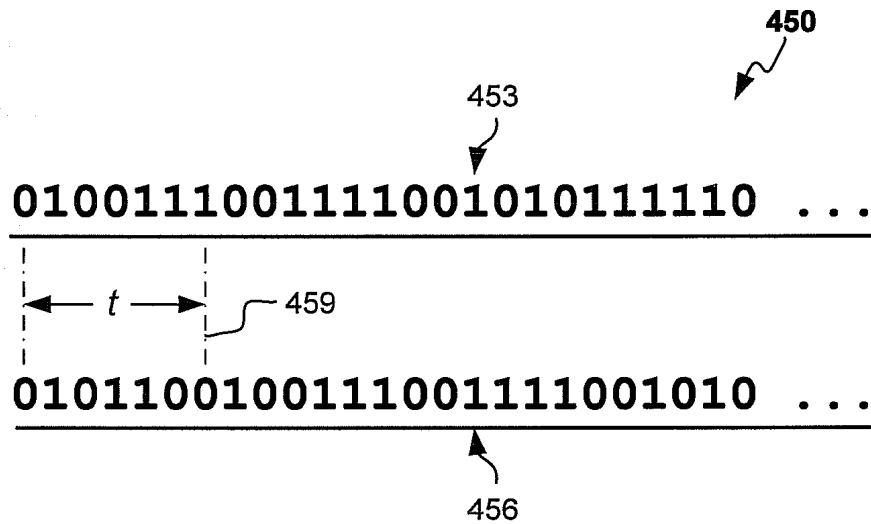


Fig. 10

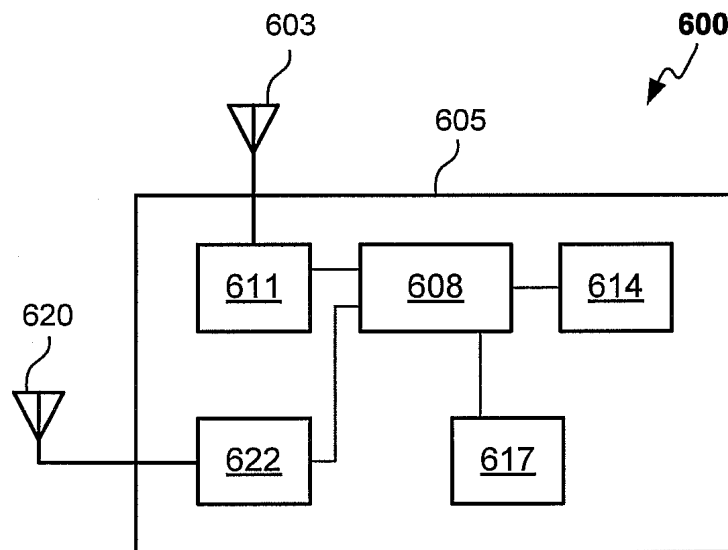


Fig. 11